

Fig.1

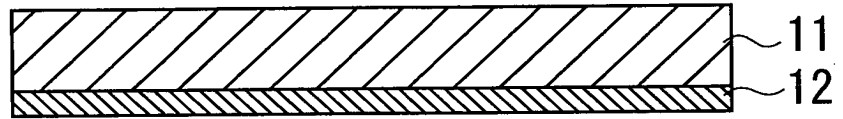


Fig.2

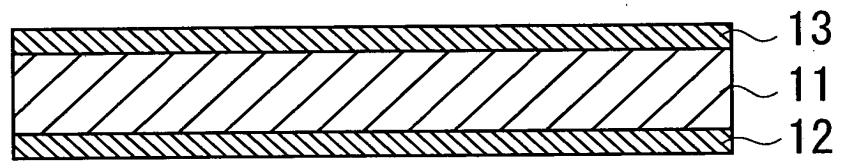


Fig.3

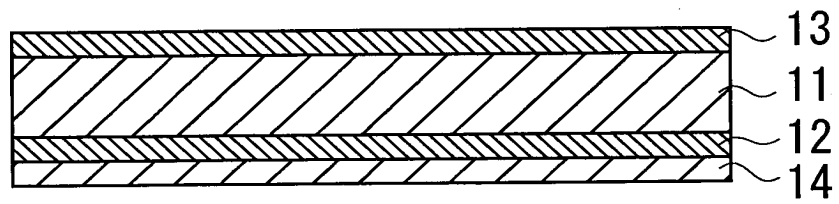


Fig.4

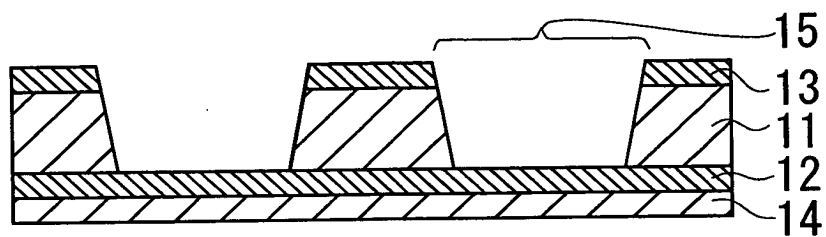


Fig.5

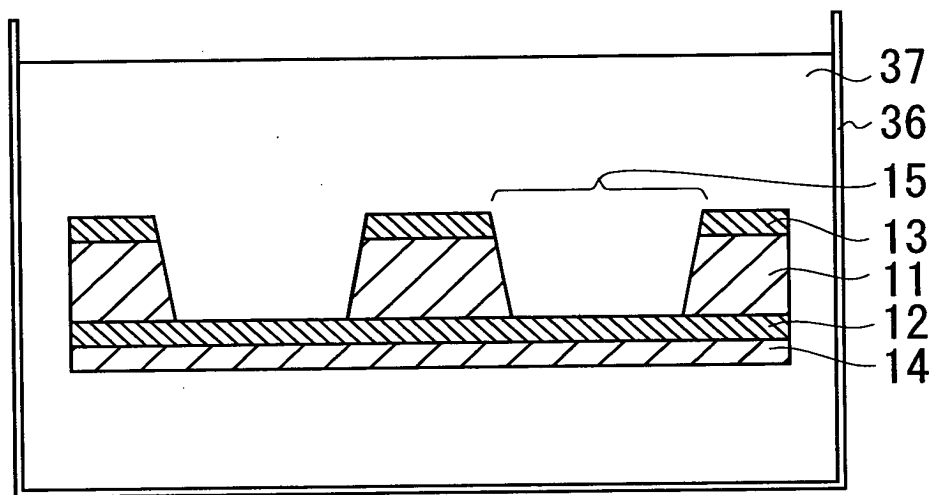


Fig.6

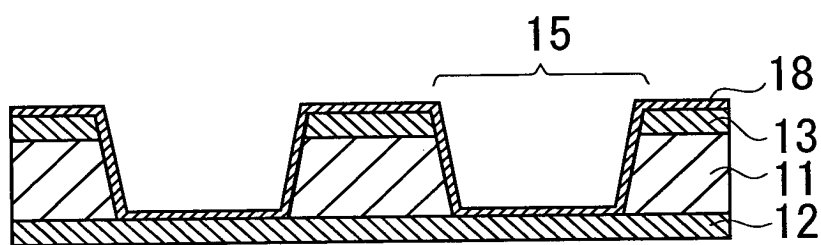


Fig.7

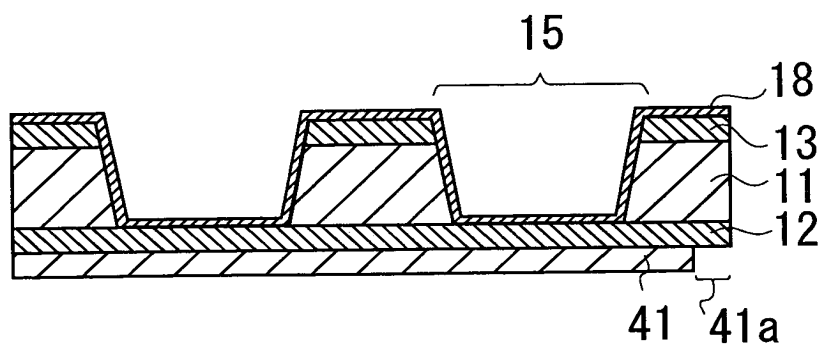


Fig.8

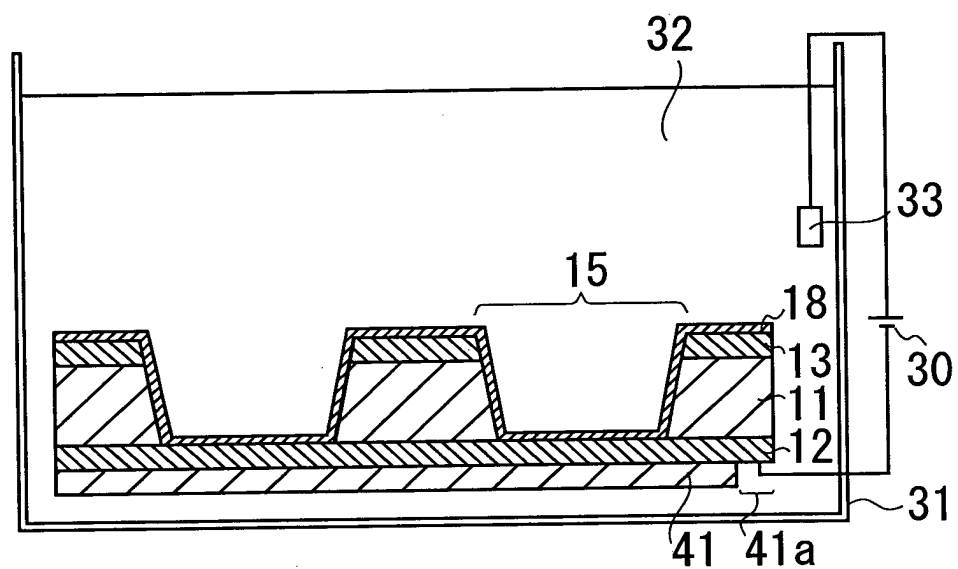


Fig.9

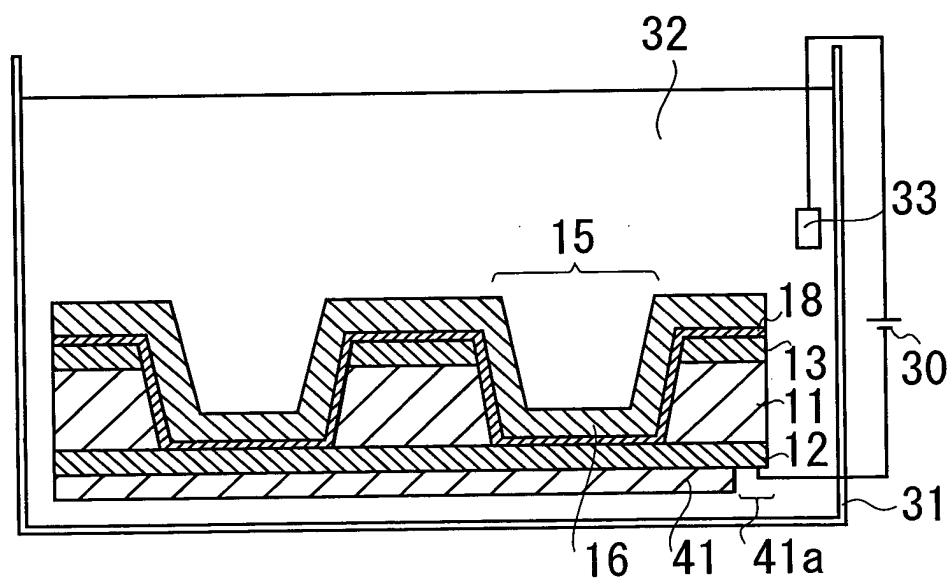


Fig.10

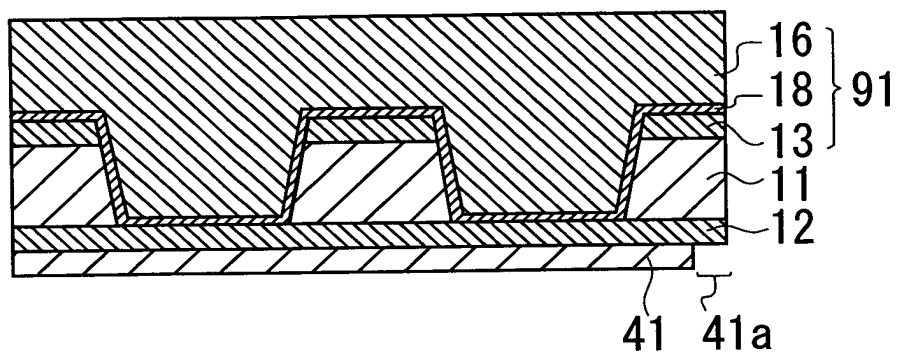


Fig.11

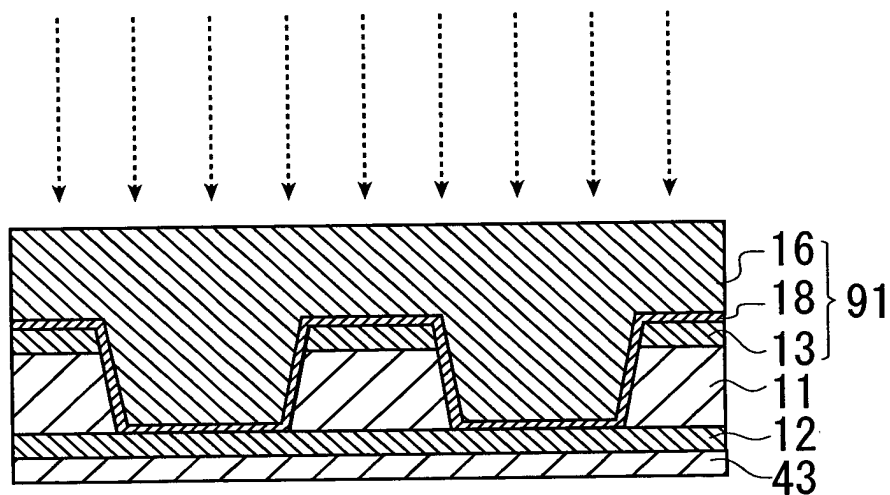


Fig.12

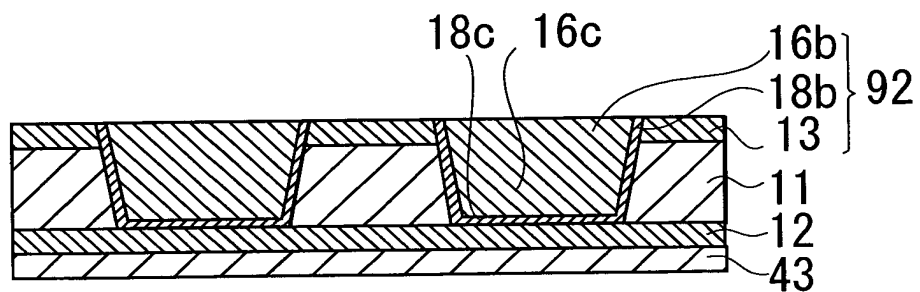


Fig.13

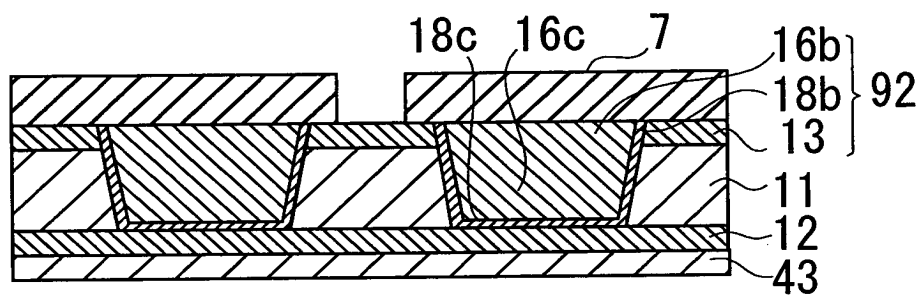
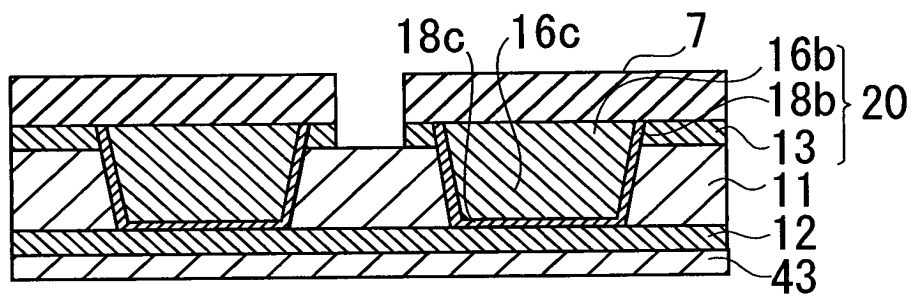


Fig.14



1

Fig.15

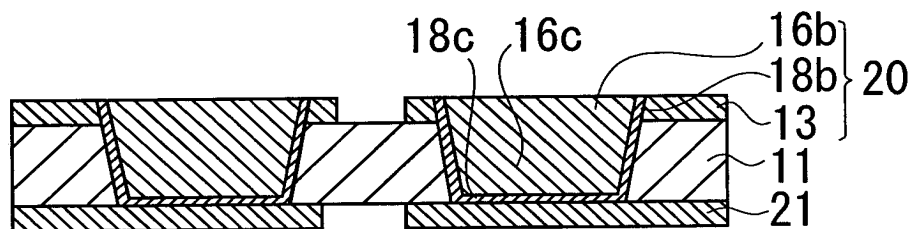


Fig.16A

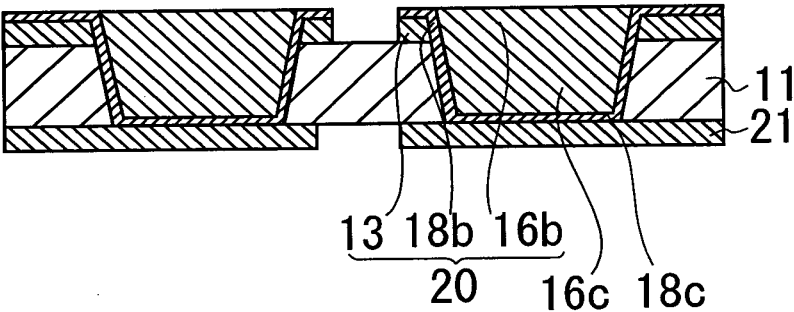


Fig.16B

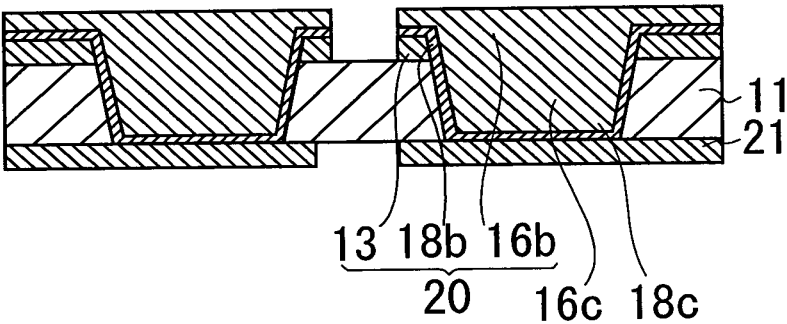


Fig.17

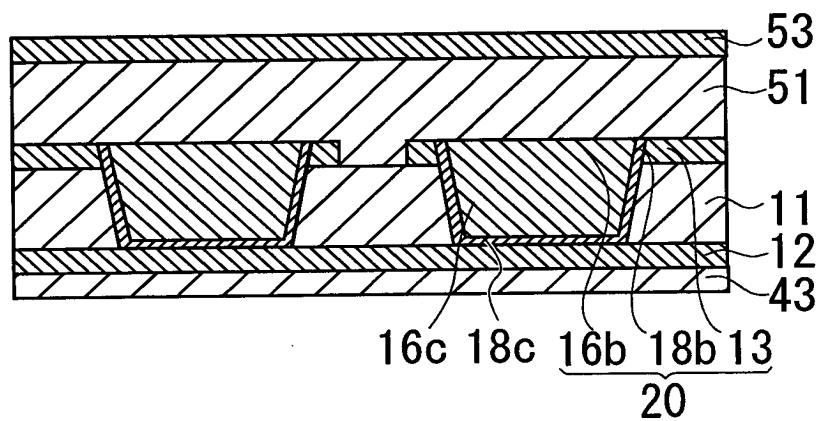


Fig.18

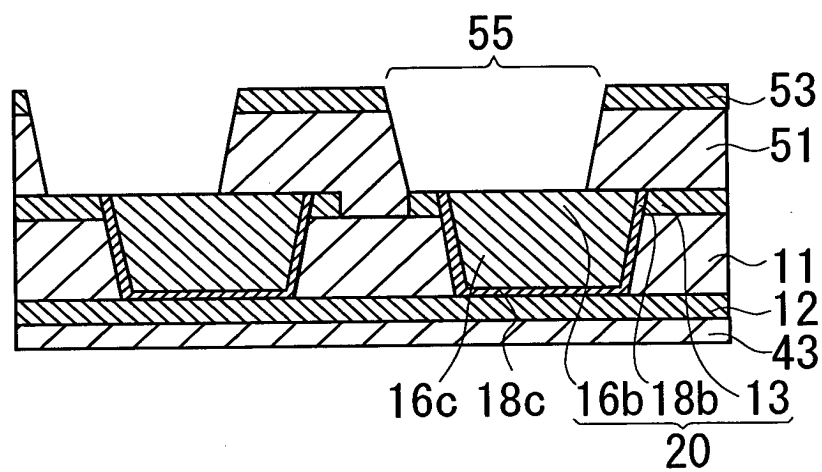


Fig.19

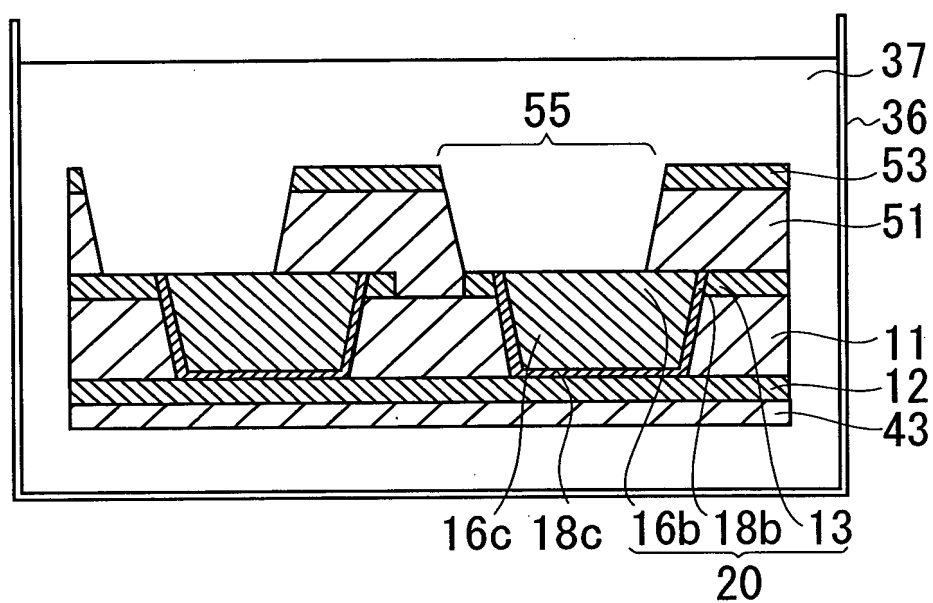


Fig.20

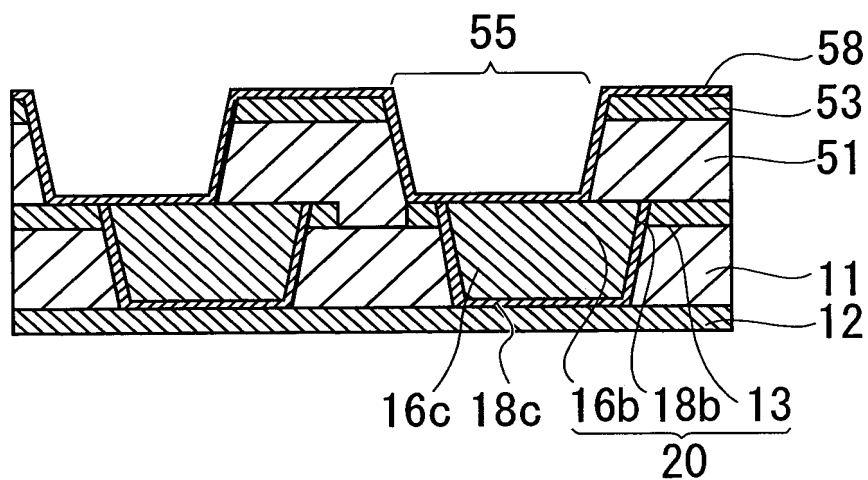
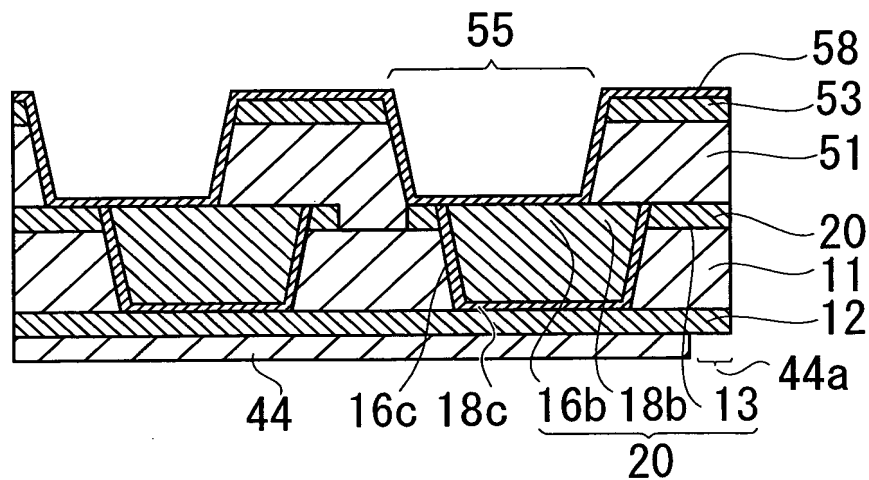


Fig.21



This cross-sectional view shows a semiconductor device with a multi-layered structure. A central cavity (55) is formed within the device. The structure includes a base layer (11) and a top layer (12). A central region (13) is defined by a series of vertical walls (16b, 16c, 18b, 18c) and a bottom layer (20). The walls are formed by a sequence of layers (44, 44a, 51, 53, 56). A central region (58) is located above the cavity. A small rectangular feature (33) is located on the right side of the device.

Fig.24

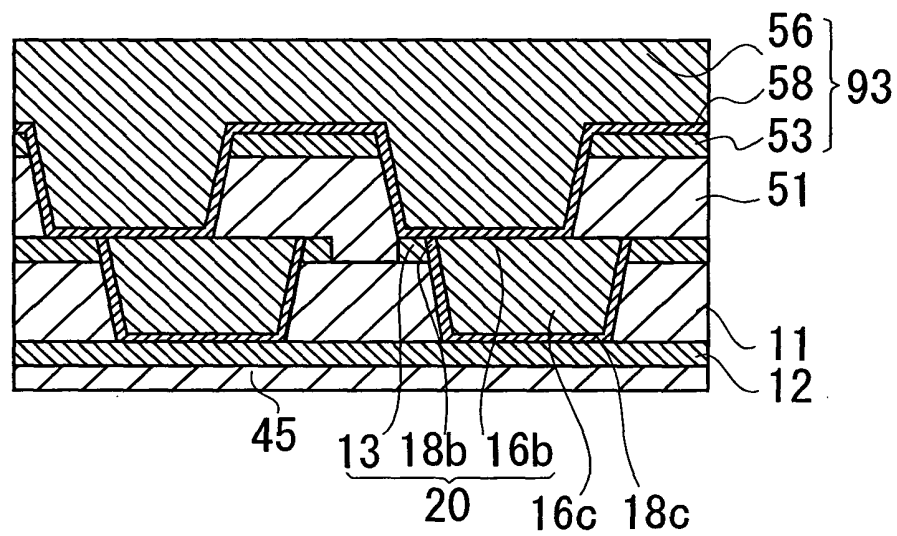


Fig.25

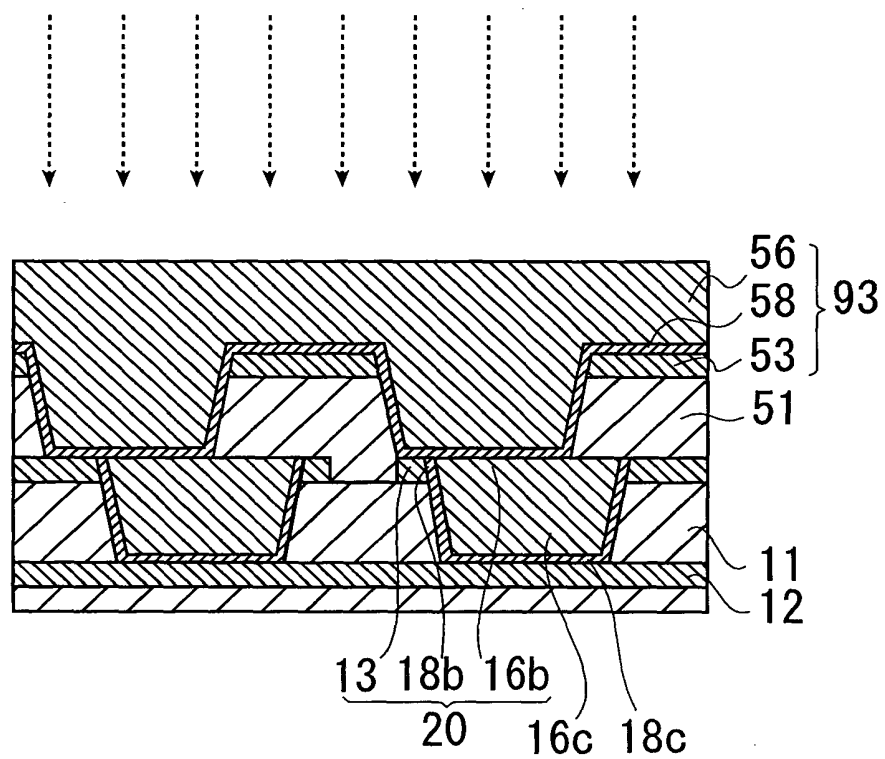
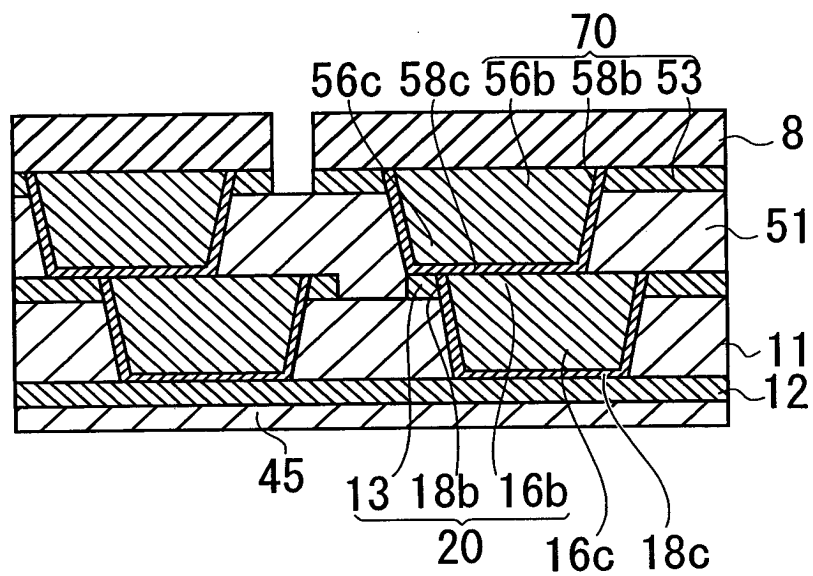


Fig.27



2

Fig.28

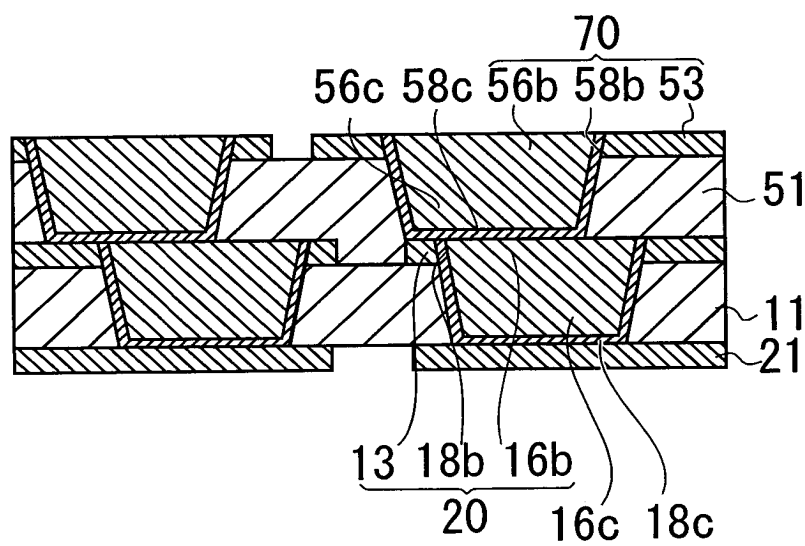


Fig.29

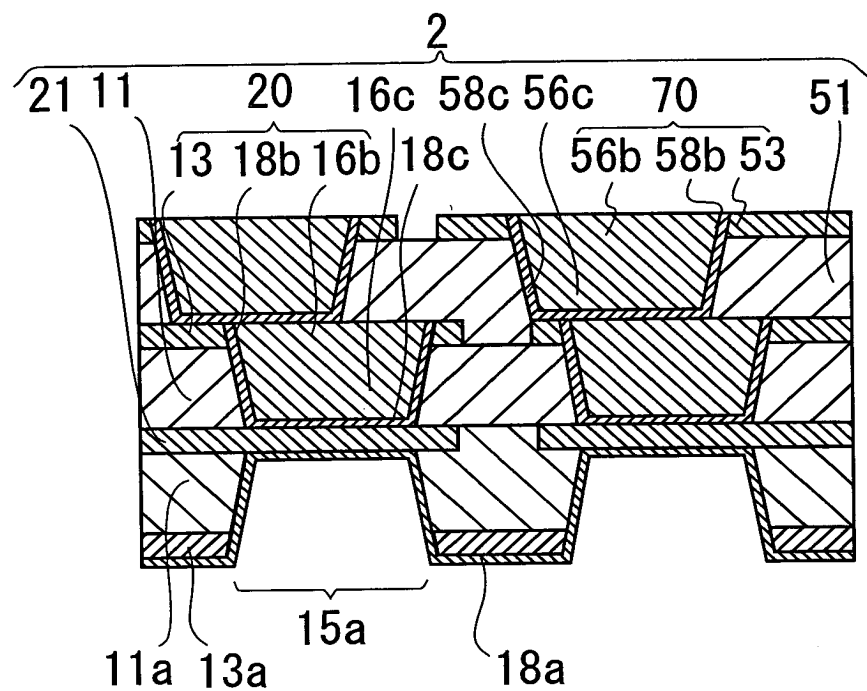


Fig.30

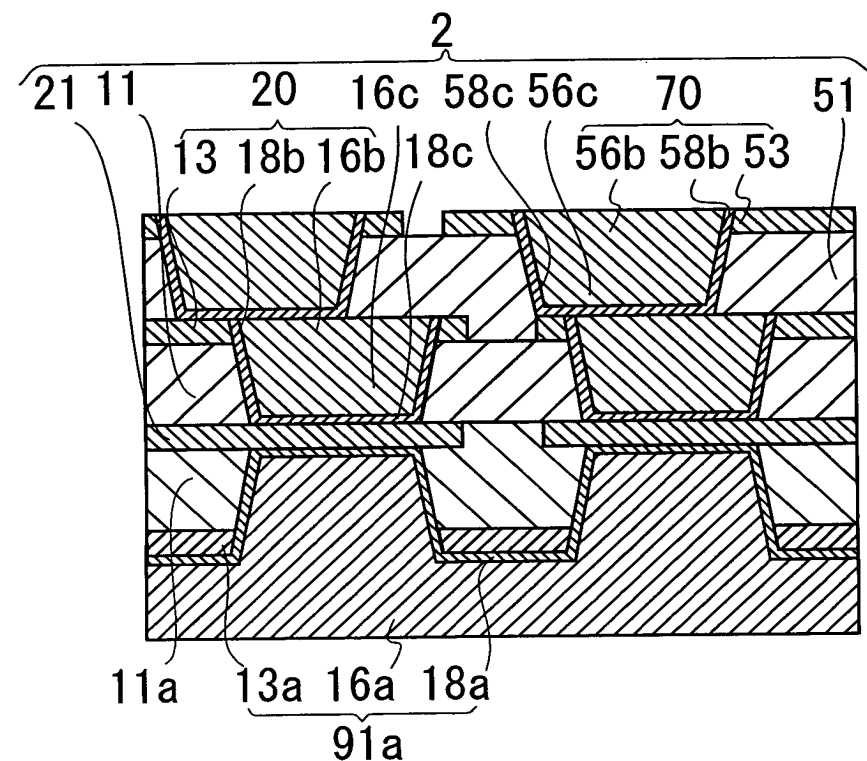


Fig.31

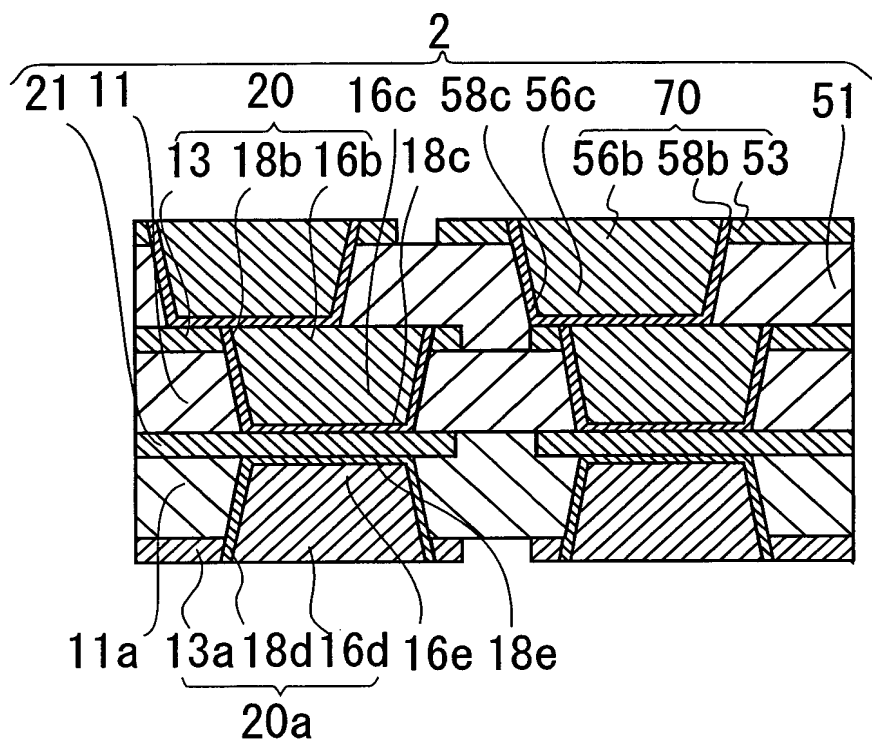


Fig.32

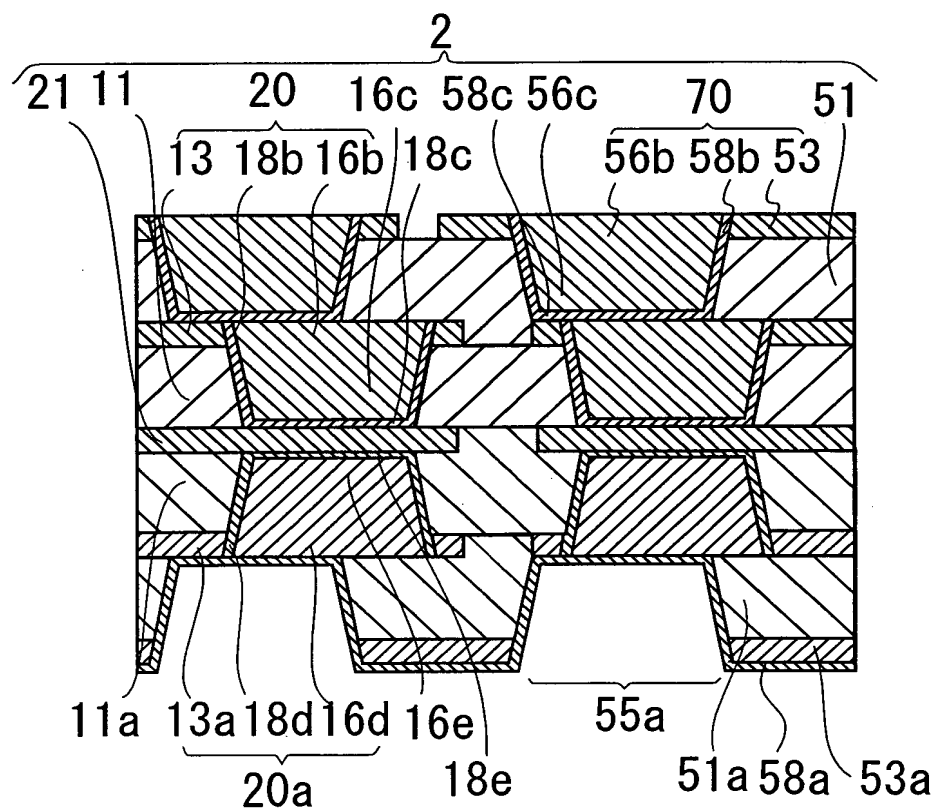


Fig.33

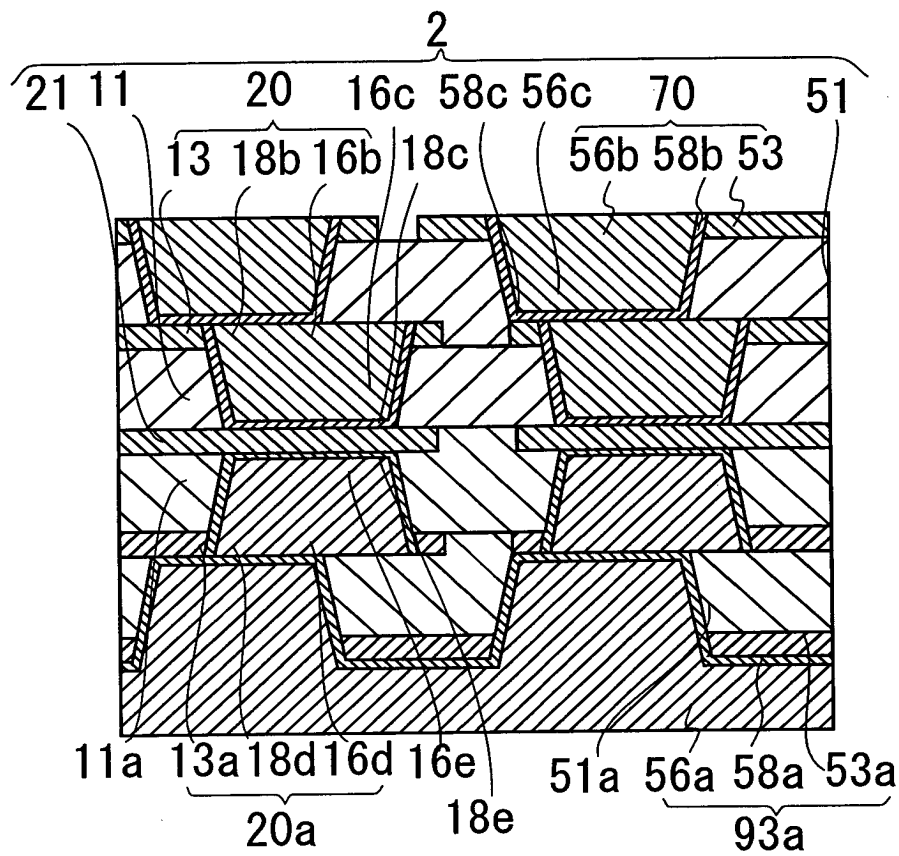


Fig.34

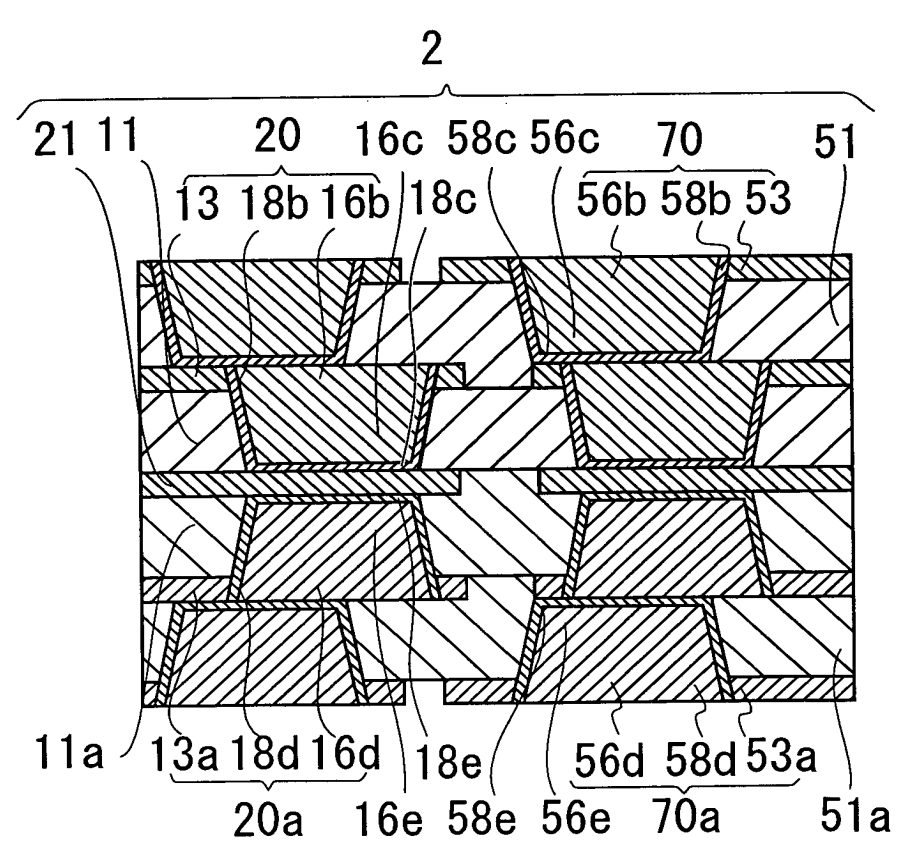


Fig.35

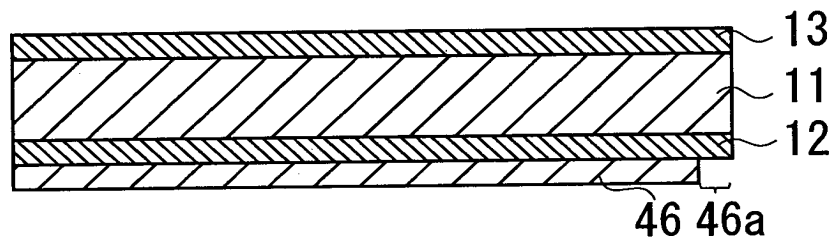


Fig.36

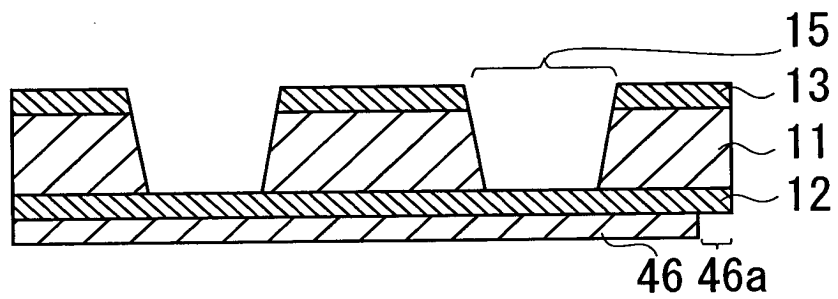


Fig.37

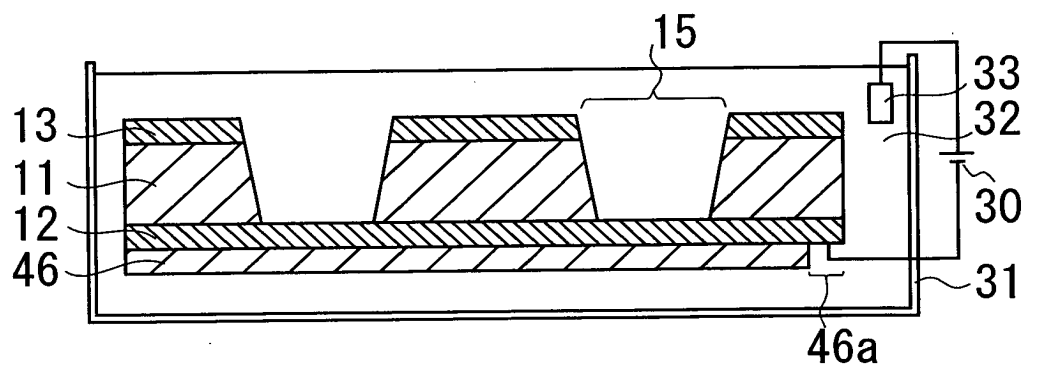


Fig.38

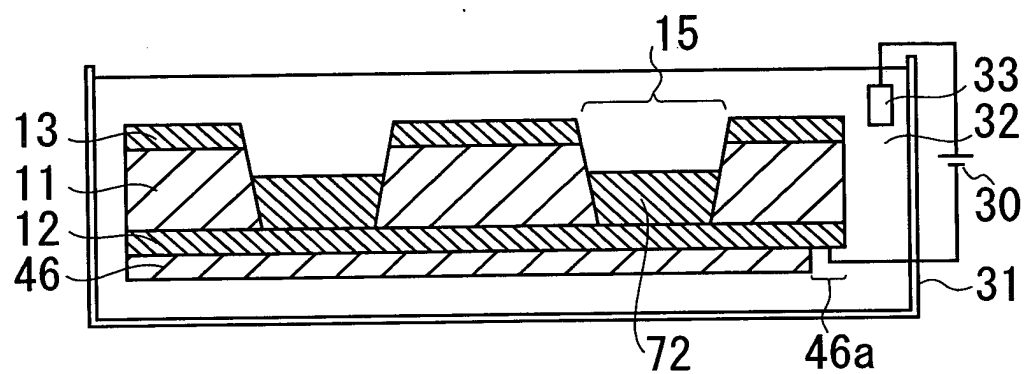


Fig.39

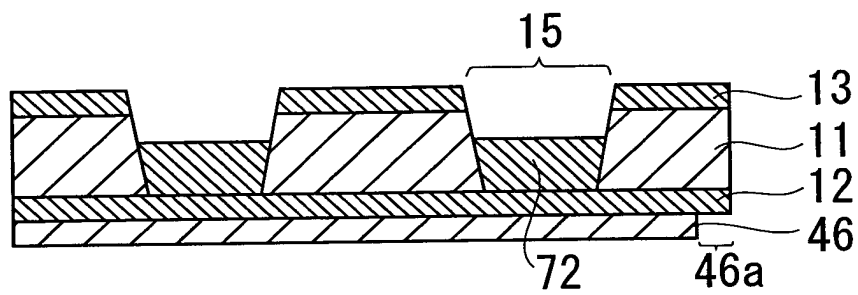


Fig.40

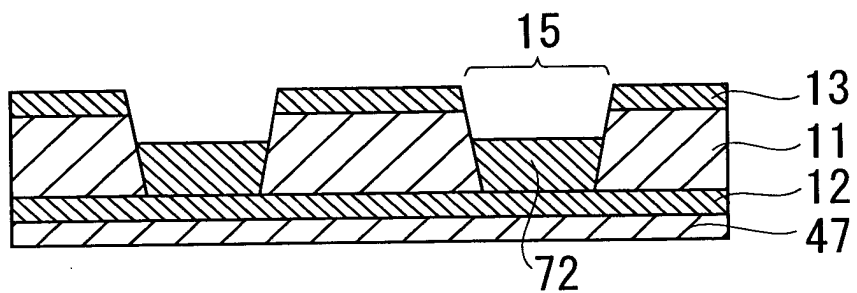


Fig.41

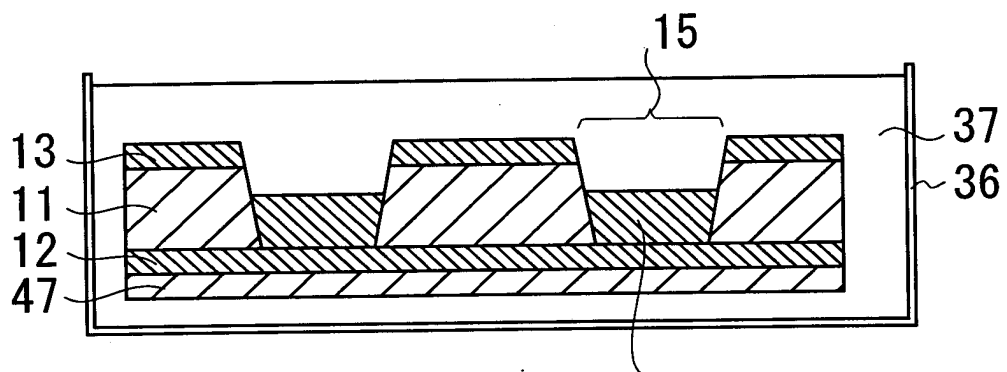


Fig.42

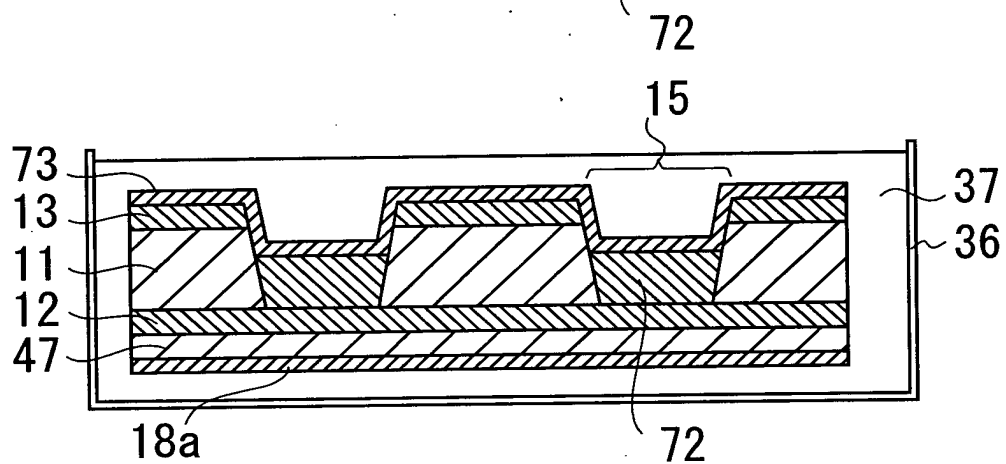


Fig.43

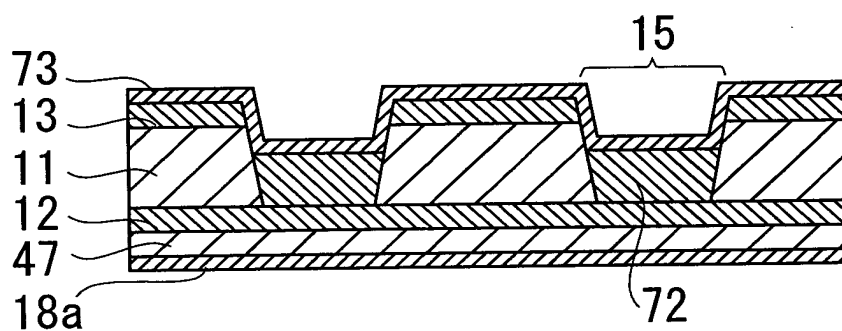


Fig.44

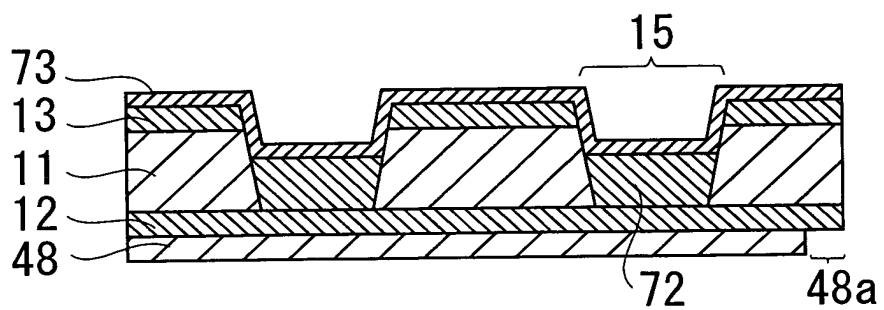


Fig.45

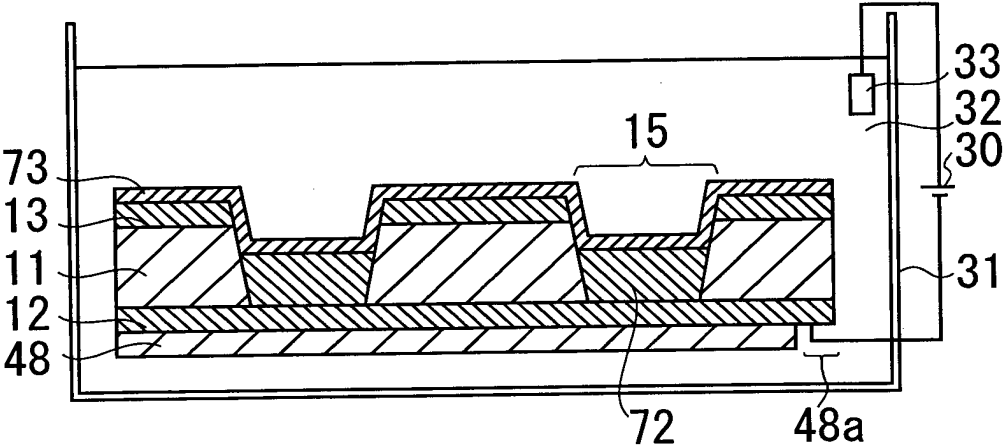


Fig.46

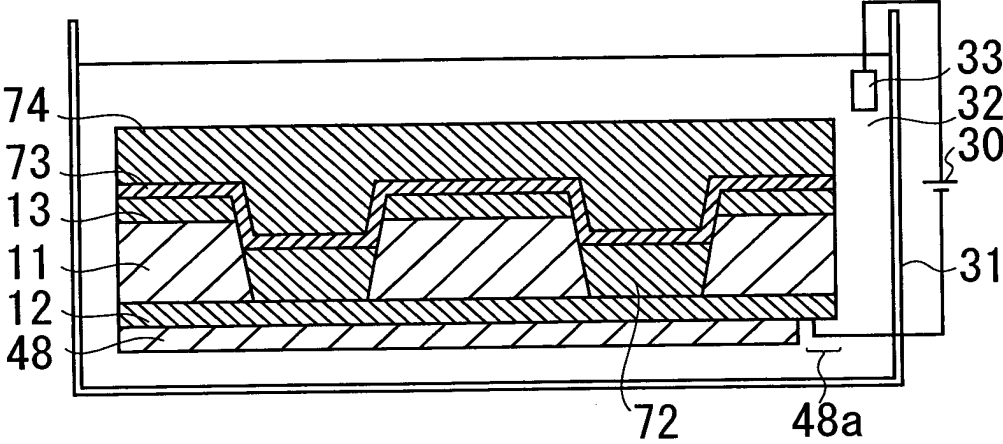


Fig.47

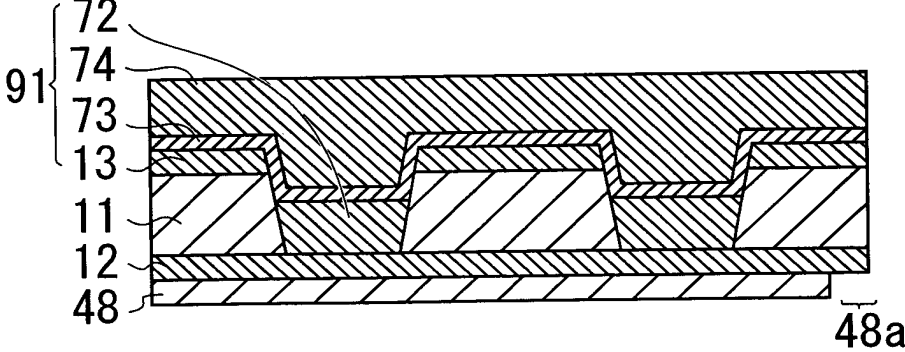


Fig.48

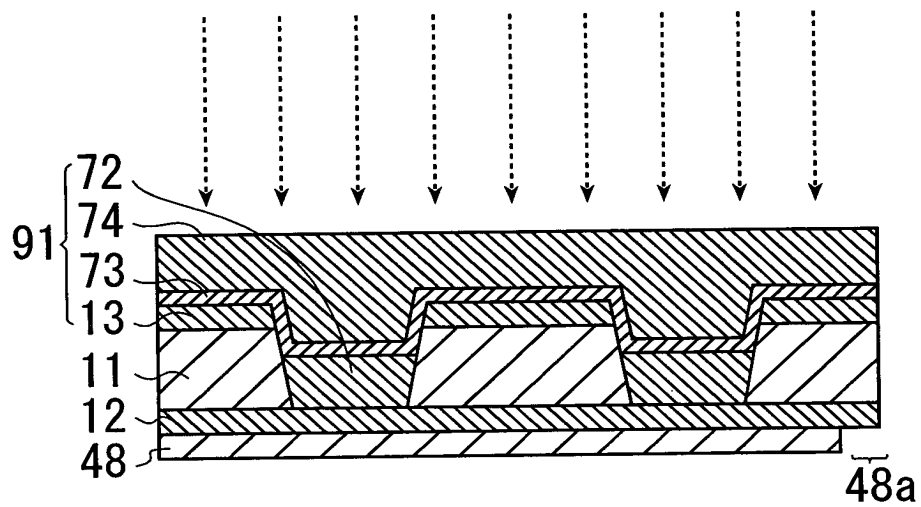


Fig.49

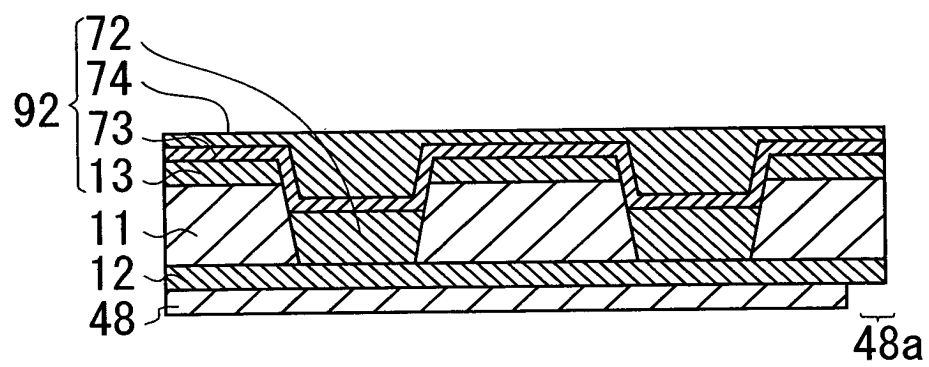


Fig.50A

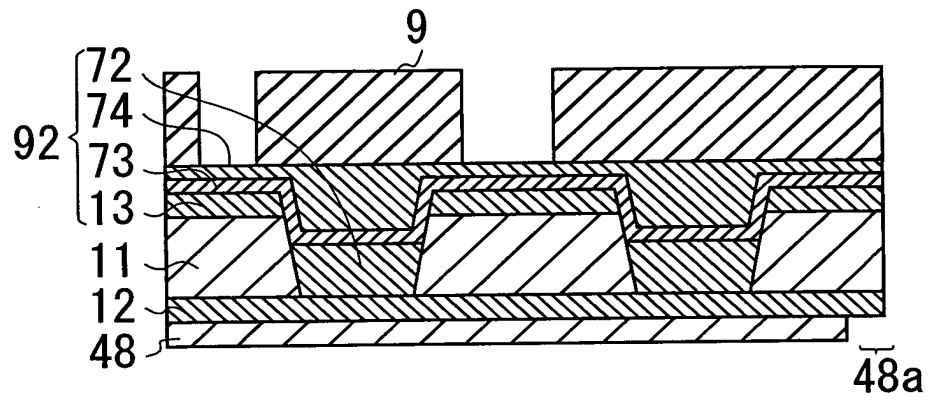


Fig.50B

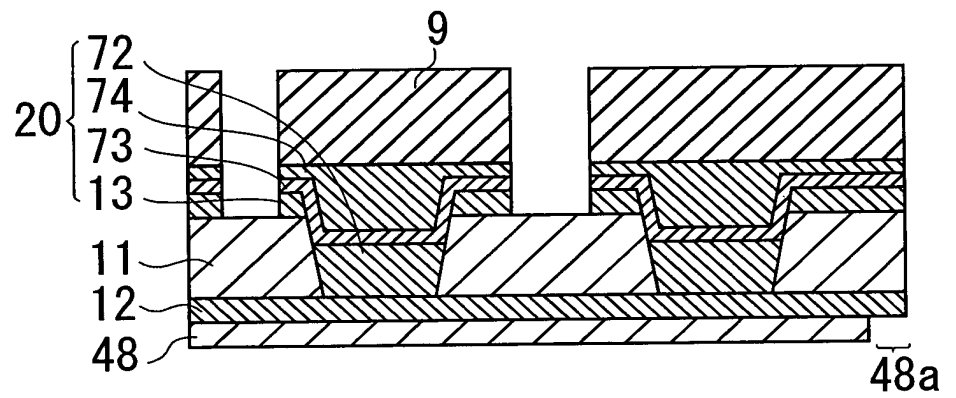


Fig.51

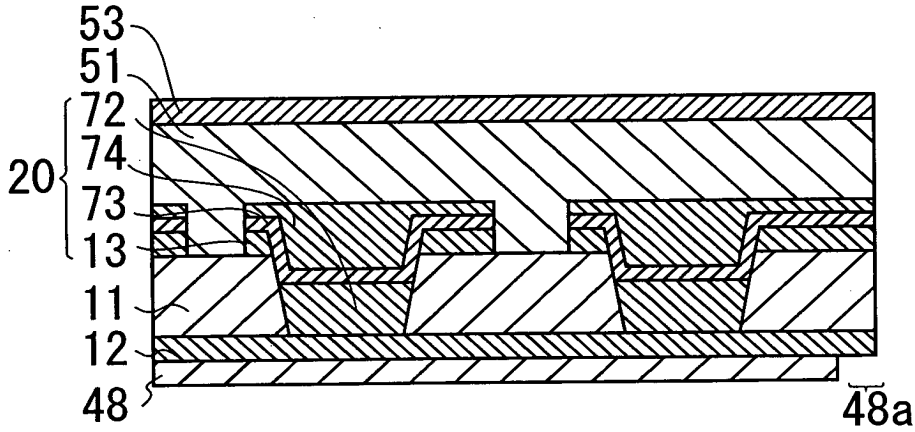


Fig.52

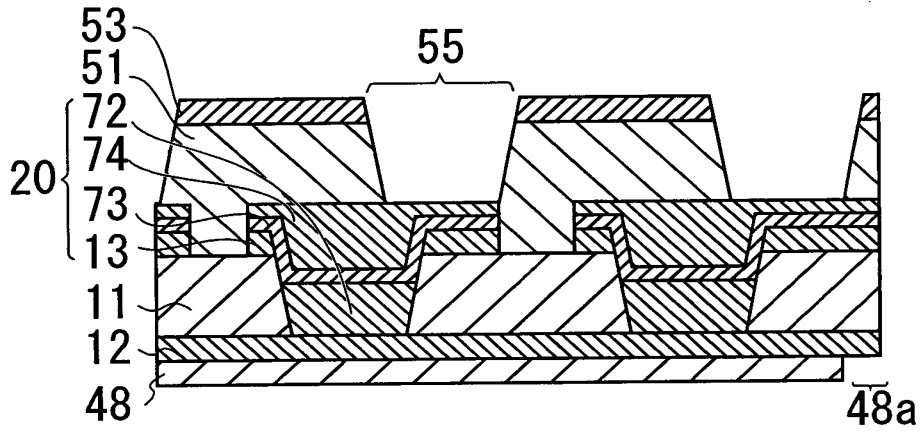


Fig.53

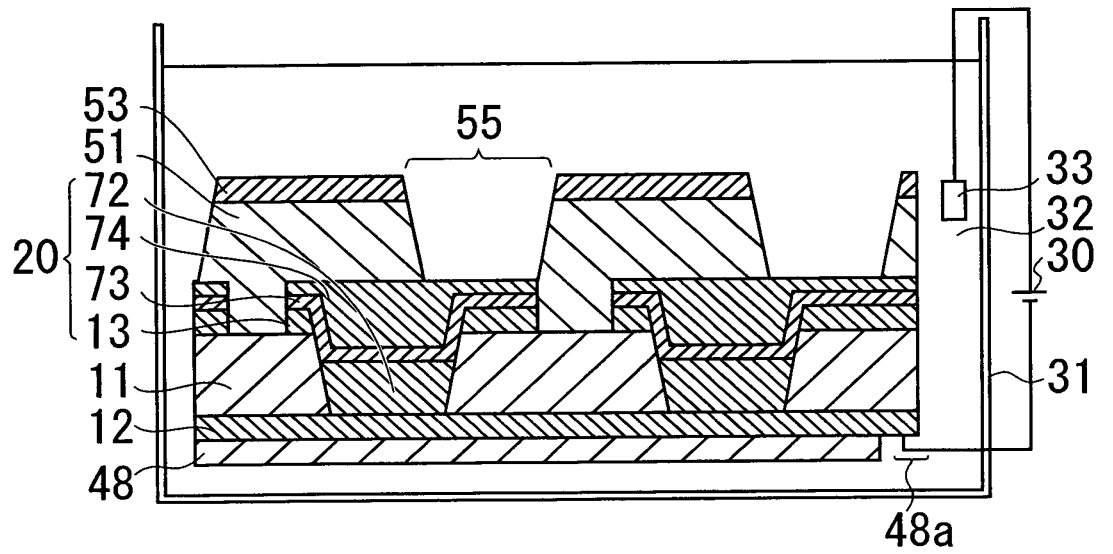


Fig.54

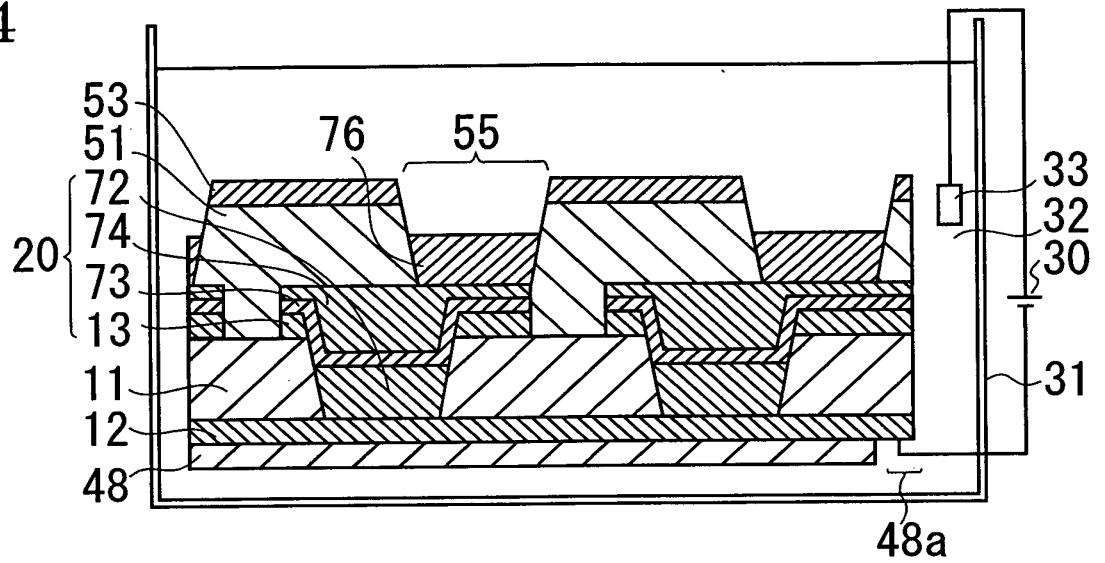


Fig.55

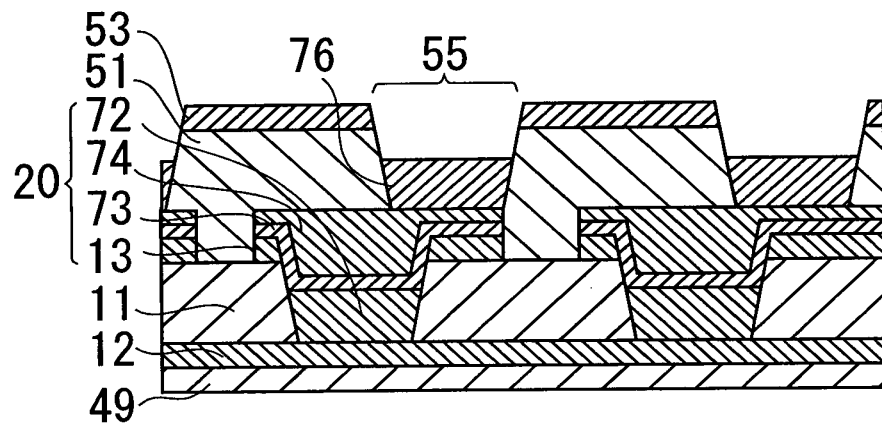


Fig.56

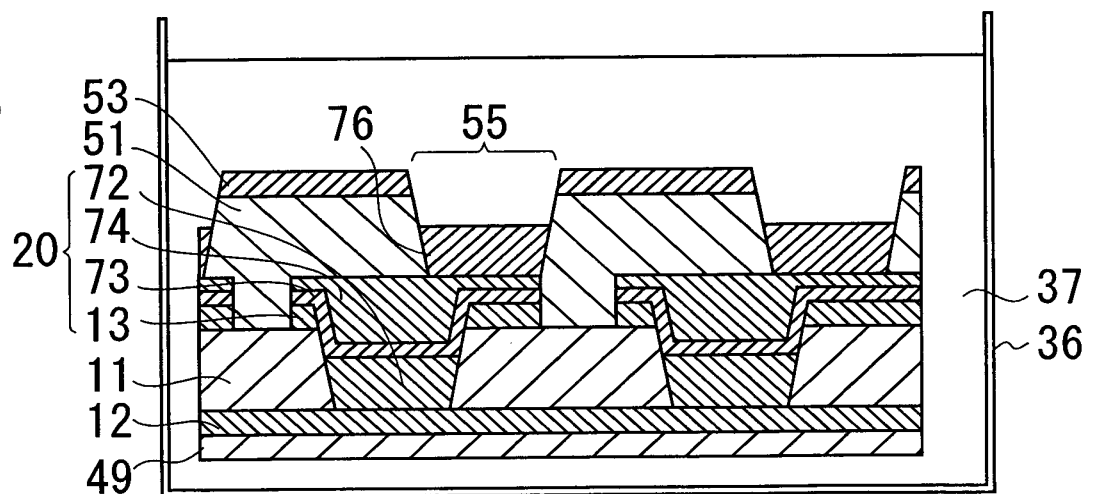


Fig.57

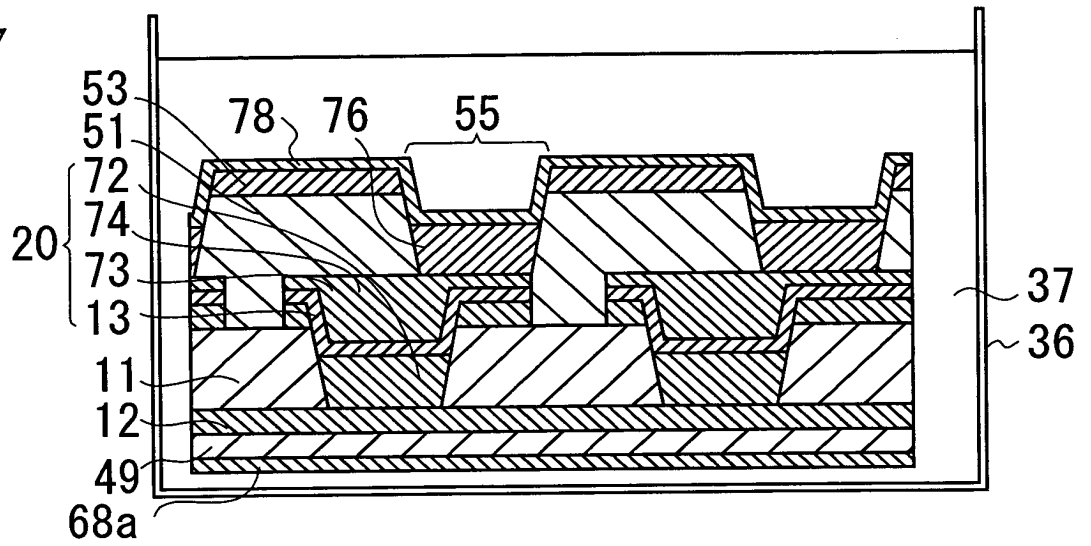


Fig.58

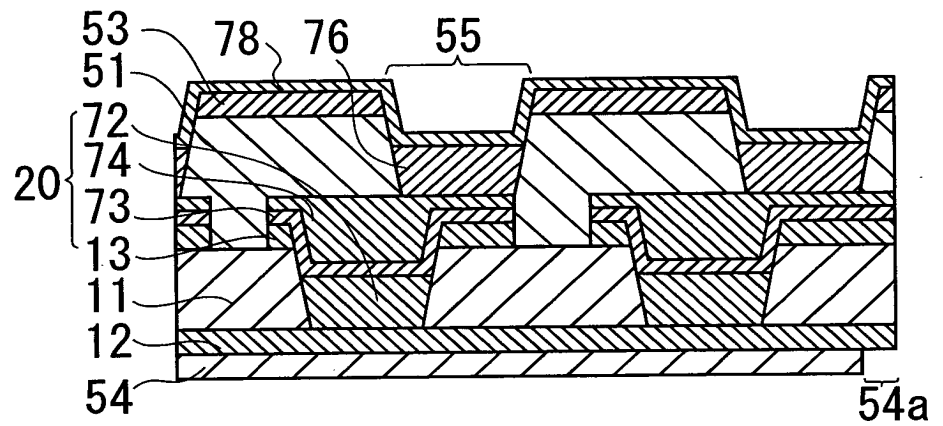


Fig.59

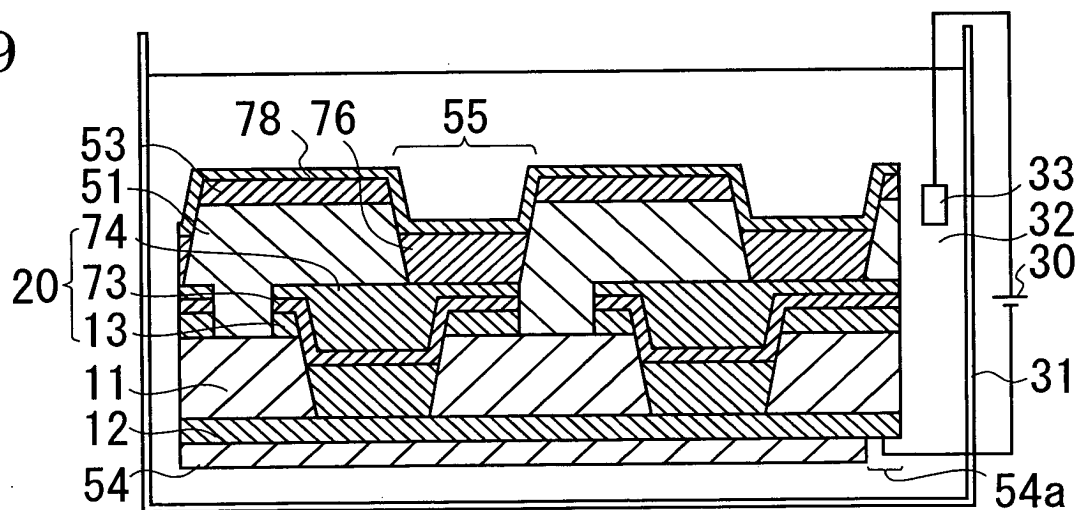


Fig.60

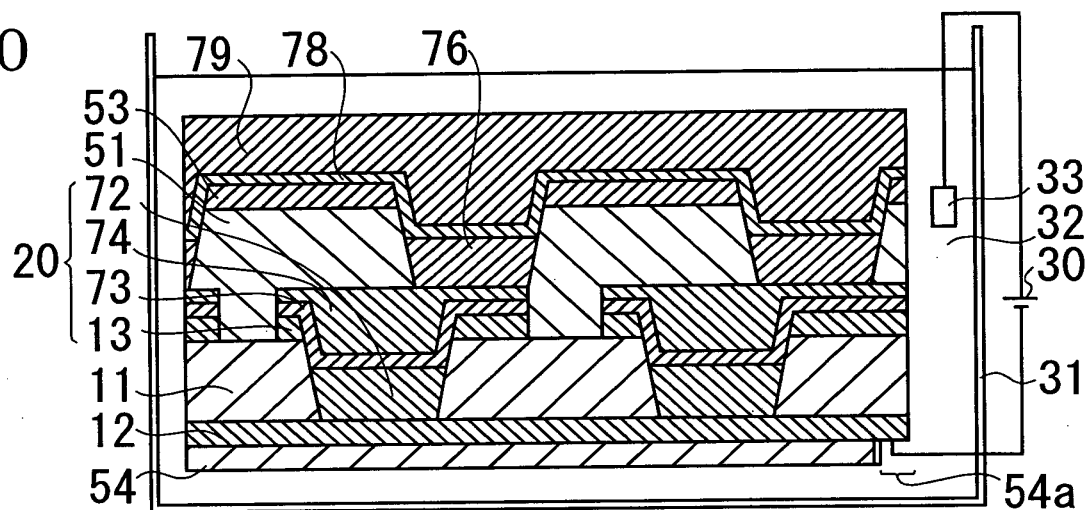


Fig.61

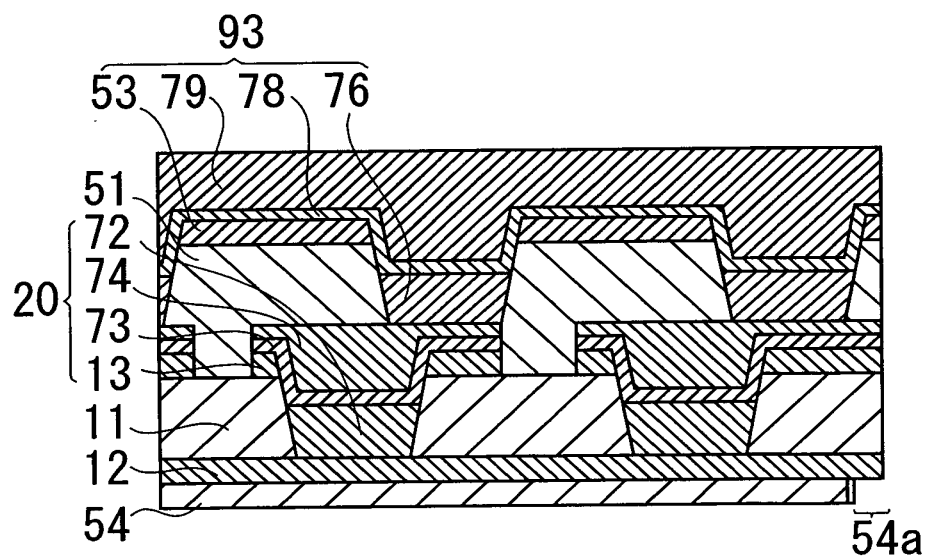


Fig.62

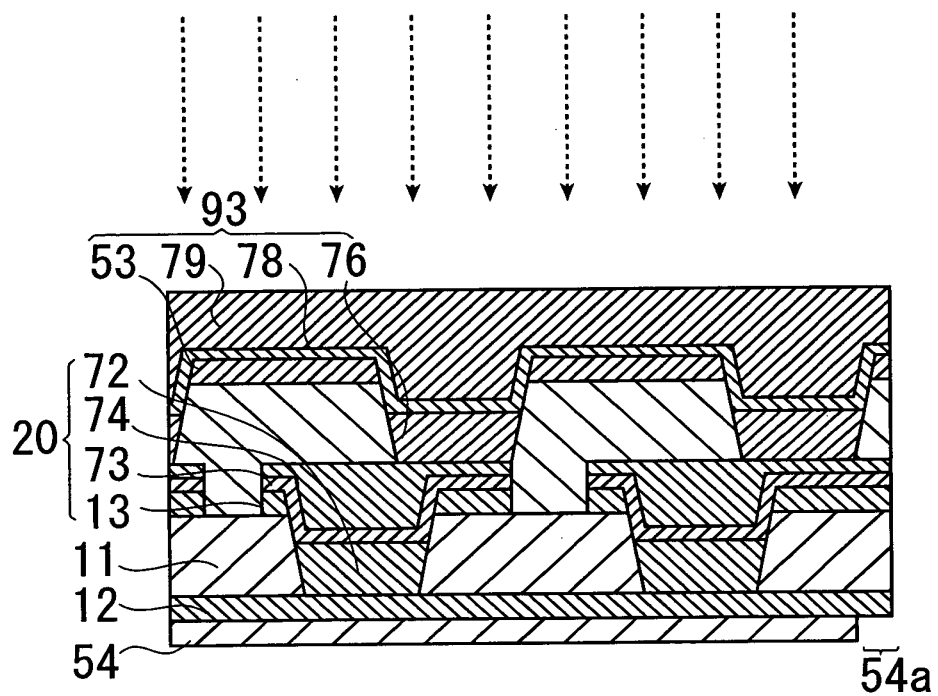


Fig.63

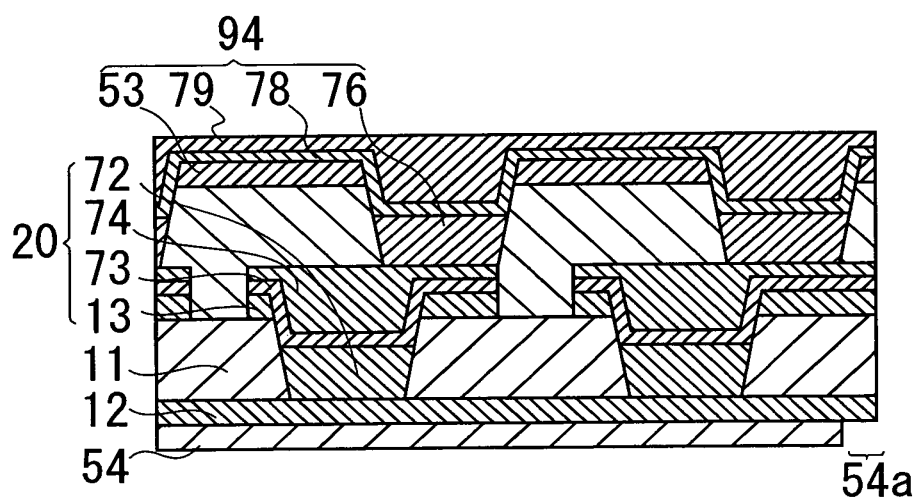


Fig.64A

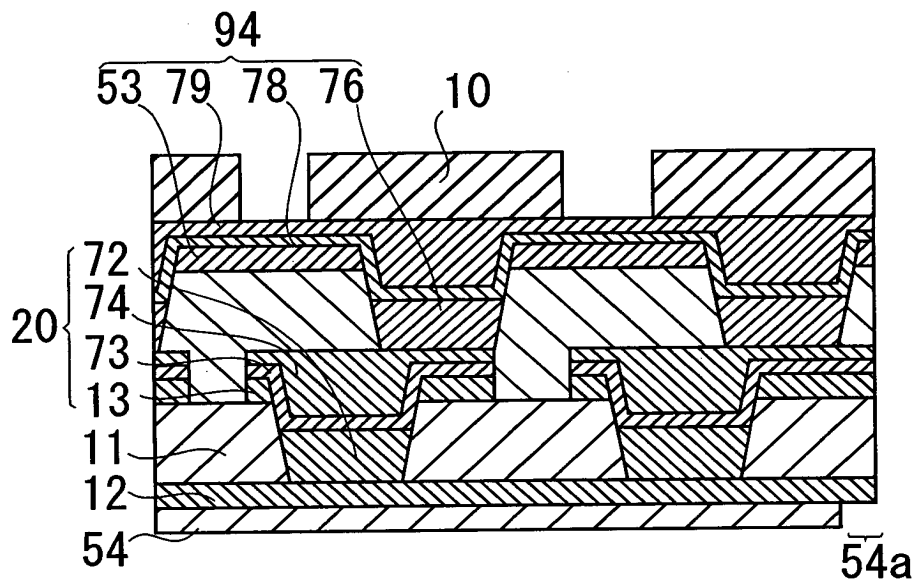
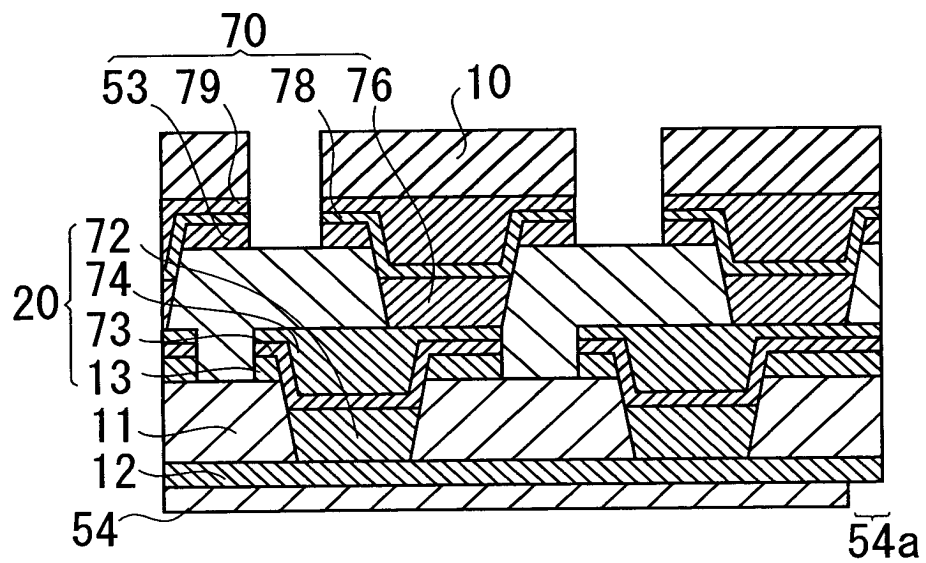


Fig.64B



5

Fig.65

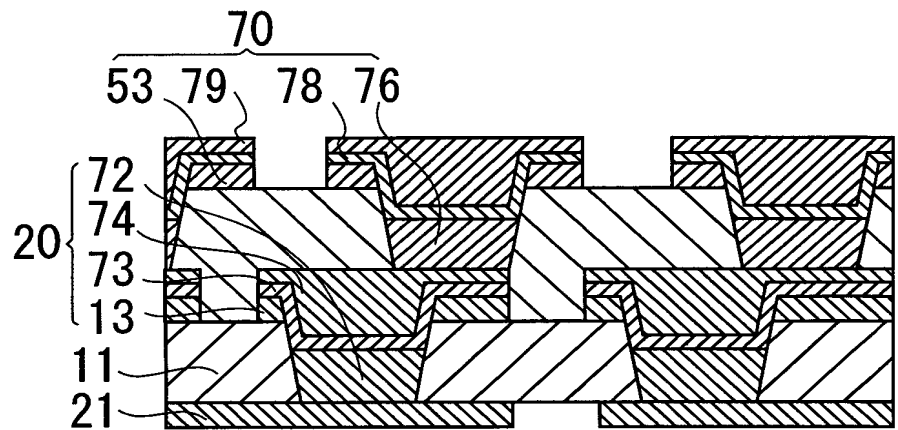


Fig.66
PRIOR ART

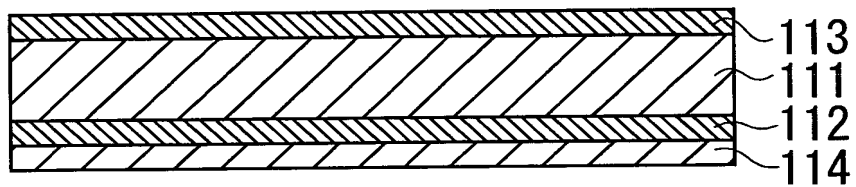


Fig.67
PRIOR ART

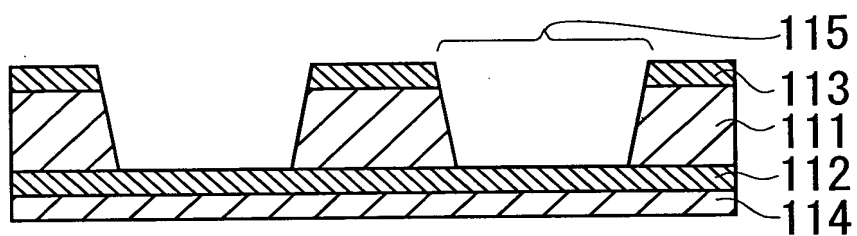


Fig.68
PRIOR ART

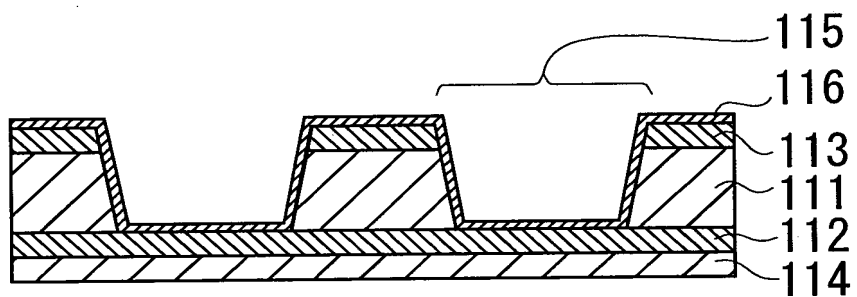


Fig.69
PRIOR ART

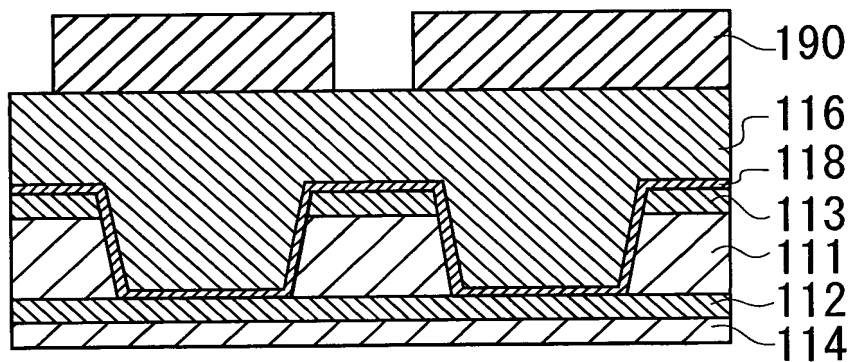


Fig.70
PRIOR ART

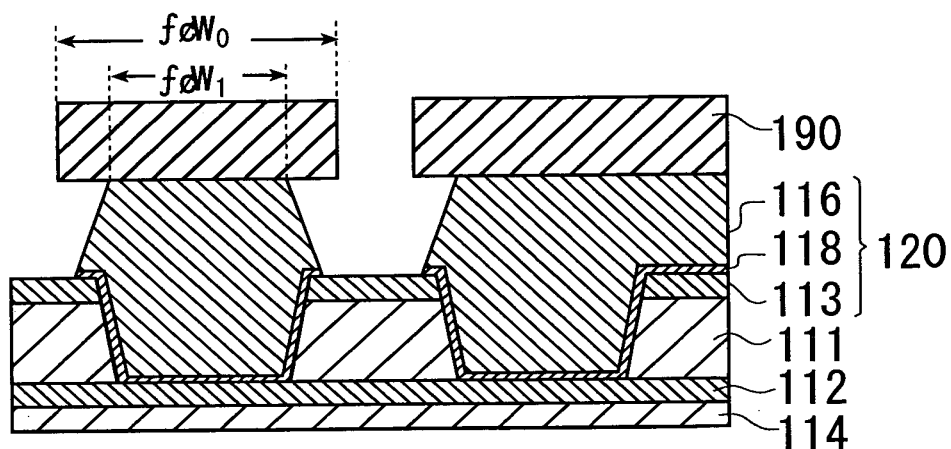


Fig.71
PRIOR ART

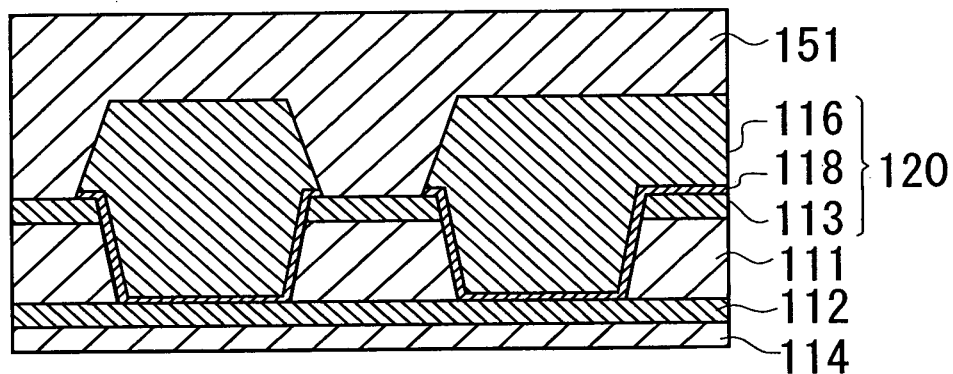


Fig.72
PRIOR ART

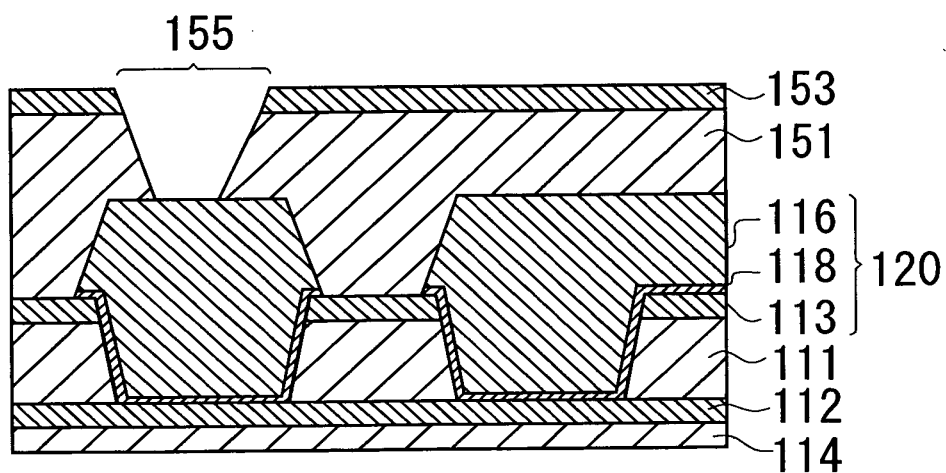


Fig.73
PRIOR ART

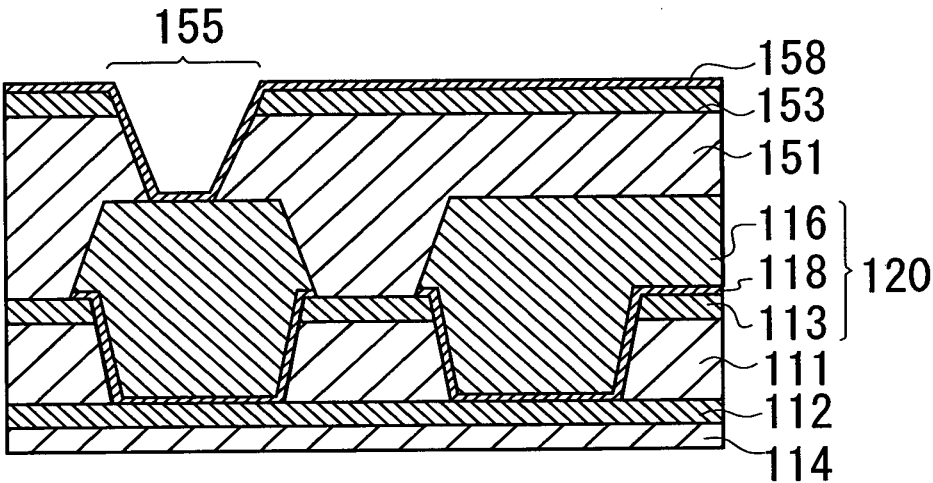


Fig.74
PRIOR ART

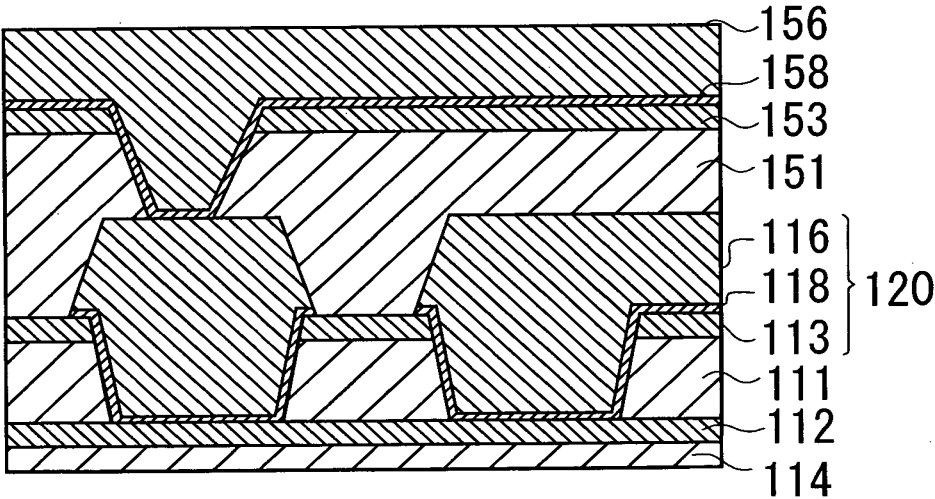


Fig.75
PRIOR ART

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